### Lecture 17

#### Pete Manolios Northeastern

**Computer-Aided Reasoning, Lecture 17** 

## Reasoning About Systems

inputs

S

outputs

- System S
- Specification f
- S is correct if it satisfies f
  - f is a claim about the observable behaviors of S
- Once we agree on *f*, the internals of *S* can be ignored
  - Typically, *f* is much simpler than *S*
  - Specs are reusable, allow for separation of concerns, ...
- Specifications should be as simple as possible to minimize the cognitive load required for humans to accept them

## Sorting Specification



Ask Northeastern Logic and Computation freshmen to define a spec for sorting

## Sorting Specification



- The output is ordered
  - For all *I*, *i*, *j* s.t. *I* is a list of numbers, *i*, *j* are natural numbers and *i* < *j* < (len (sort *I*)): (sort *I*)[*i*] ≤ (sort *I*)[*j*]
- What if (sort /) is empty, but / is not?
  - For all / s.t. / is a list of numbers: (len (sort /)) = (len /)
- What if  $n \in I$ , but  $n \notin (\text{sort } I)$ ?
  - For all *I*, *n* s.t. ...:  $n \in I \Rightarrow n \in (\text{sort } I)$
- What if *n* appears *i* times in *I*, but *j≠i* times in (sort *I*) ?
  - (sort /) is a permutation of /
- Define permutation
- Are we done (are the properties complete)?
- Are the properties independent?

## Sorting Spec via Refinement



- What about
  - For all / s.t. / is a list of numbers: (sort /) = (isort /)
  - Where isort is insertion sort?
- Advantages
  - No need to define permutation, ...
  - Defining isort is simple
  - Completeness trivial
  - Independence trivial
  - Easier spec to understand and accept

## Reactive Systems

- Refinement
  - I refines S if every behavior of I is allowed by S
- Examples
  - Pipelined Microprocessors
  - Sequential Consistency
  - Communications Protocols
  - Event Processing Systems
  - Distributed Databases



#### Hardware Verification: Motivation



International Technology Roadmap for Semiconductors, 2004 Edition.

Verification has become the dominant cost in the design process. On current projects, verification engineers outnumber designers, with this ratio reaching two or three to one for the most complex designs.

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Without major breakthroughs, verification will be a non-scalable, show-stopping barrier to further progress in the semiconductor industry.

The overall trend from which these breakthroughs will emerge is the shift from ad hoc verification methods to more structured, formal processes.

### Hardware Verification Challenge

- Verification costs range from 30%-70% of the entire design cost.
- R&D for typical CPU: 500+ team, costing \$0.5-1B.
- Pentium 4 (Bob Bently CAV 2005).
  - Full-chip simulation ~20Hz on Pentium 4.
  - Used ~6K CPUs 24/7: ~3 years later <1 minute of simulation cycles.</p>
  - Exhaustive testing is impossible.
  - First large-scale formal verification at Intel: 60 person years.
  - Checked over 14K properties: Decode, Floating point, pipeline.
  - Found bugs, but no silicon bugs found to date in these areas.

Pentium FDIV (Floating point DIVision) bug in Pentium 386 led to a **\$475 million** write-off by Intel Bob Bently CAV **\$12B** 2005 terms



#### Outline

- Pleasantness Problem
- Refinement
- Local Reasoning
- Pipelined Machine Verification
- Automating Refinement
- Refinement Map Factor
- Compositional Reasoning
- Deductive Methods & Decision procedures
- Conclusions

#### **Approaches to Verification**

#### Property Based.

- Relational, e.g., sorting.
- Temporal logic, e.g., reactive systems.

#### Refinement.

- I refines S if every behavior of I is allowed by S.
- The dual of abstraction.
- Sorting.
- Pipelined machines.
- Communications Protocols.
- Distributed Databases.

# Key Concepts in Refinement

- Refinement: *I* refines *S* if every behavior of *I* is allowed by *S*
- Identify what is *observable* 
  - Transformational systems: input & output
  - Reactive systems: refinement maps, e.g., *I* may contain more components and may use different data representations than *S*
- Theoretical, *semantic-based* approach
  - Use Kripke structures  $M = \langle S, \dots, L \rangle$
  - Language agnostic
- Direct support for *Stuttering* 
  - I may require several steps to match S

#### Refinement, the Picture



Formal connection between different abstraction levels.

Compositional.

Preservation of safety and liveness.

Avoid "leaky abstractions."

### Behaviors

I refines S if every behavior of I is allowed by S What are the behaviors of a system?

- Linear time
  - Programs and properties are sets of infinite sequences
  - Trace containment, equivalence (PSPACE-complete)
  - LTL
- Branching time
  - Programs and properties are sets of infinite trees
  - Simulation, bisimulation (in P)
  - ACTL\*, CTL\*, μ-calculus



## Existence of Refinement Maps

Abadi and Lamport. *The Existence of Refinement Mappings,* Theoretical Computer Science, 1991.

- Spec: state machine & supplementary property (fairness)
- Proving that *I* refines *S* requires reasoning about infinite sequences: if *I* allows  $\langle \langle e_0, z_0 \rangle$ ,  $\langle e_1, z_1 \rangle$ ,  $\langle e_2, z_2 \rangle$ , ... $\rangle$ , then *S* allows  $\langle \langle e_0, y_0 \rangle$ ,  $\langle e_1, y_1 \rangle$ ,  $\langle e_2, y_2 \rangle$ , ... $\rangle$
- Reason *locally* (structurally)!
- Definition: If  $f(e_n, z_n) = \langle e_n, y_n \rangle$ , *f* can be used to prove, locally, that *I* preserves safety properties of *S*. If *f* preserves liveness, then it is a *refinement mapping*.
- Theorem: If the machine-closed specification *I* implements *S*, a specification that has finite invisible nondeterminism and is internally continuous, then there is a specification *I<sup>h</sup>* obtained from *I* by adding a history variable and a specification *I<sup>hp</sup>* obtained from *I<sup>h</sup>* by adding a prophecy variable such that there exists a refinement mapping from *I<sup>hp</sup>* to *S*.

# My Refinement Results

- A compositional theory of refinement that deals with liveness
- Branching time
  - Theorem: If *I* implements *S*, there exists a refinement mapping from *I* to *S*.
- Linear time
  - Theorem: If *I* implements *S*, then there is a specification *I*°, obtained from *I* by adding an oracle variable, such that there exists a refinement mapping from *I*° to *S*.
- Proofs show how to construct refinement maps & provide key insights for mechanization

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#### Refinement

Transition System (TS)  $\mathcal{M} = \langle S, \rightarrow , L \rangle$ Let  $-\mathcal{M} = \langle S, \rightarrow , L \rangle$  (the implementation)

 $-\mathcal{M}' = \langle S', \rightarrow ', L' \rangle \quad \text{(the specification)} \\ -r: S \to S'$ 

- We say that  $\mathcal{M}$  is a *simulation refinement* of  $\mathcal{M}$ ' with respect to refinement map *r*, written  $\mathcal{M} \sqsubseteq_r \mathcal{M}$ ', if there exists a relation, *B*, such that:
  - $\langle \forall s \in S :: sB(r.s) \rangle$
  - *B* is an STS on the TS  $\langle S \Downarrow S', \dashrightarrow \Downarrow \land \bot \rangle$ , where

 $\mathcal{L}.s = L'(s)$  for s an S' state, else  $\mathcal{L}.s = L'(r.s)$ .

• Compositional:  $\mathcal{M} \sqsubseteq_r \mathcal{M}' \land \mathcal{M}' \sqsubseteq_q \mathcal{M}'' \implies \mathcal{M} \sqsubseteq_{r;q} \mathcal{M}''$ 

Transition System (TS)  $\mathcal{M} = \langle S, \rightarrow , L \rangle$ 

B is a stuttering simulation (STS) on  $\mathcal{M}$  iff for all s, w such that sBw:

1. L.s = L.w



(s)

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- **1.** L.s = L.w
- **2**.  $\langle \forall \sigma : fp.\sigma.s : \langle \exists \delta : fp.\delta.w : match(B,\sigma,\delta) \rangle \rangle$



: B

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*match*( $B,\sigma,\delta$ ):  $\sigma,\delta$  can be partitioned into non-empty, finite segments such that states in related segments are related by B.







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For every TS M, there is a greatest STS on M:
Let C be a set of STS's, then ∪<sub>B∈C</sub> is an STS.

- If B is an STS, so is B\*.
  - $B^* = \bigcup_{i \in \mathbb{N}} B^i$ .
  - The identity relation is an STS.
  - If R and S are STSs, so is R;S, their composition.
- The greatest STS, G, on  $\mathcal{M}$  is a preorder.
  - G\* is an STS.
  - $G \subseteq G^*$ , but also  $G^* \subseteq G$ .

Theorem:

Let *B* be a STS on  $\mathcal{M}$  and let *sBw*. For every ACTL\*\X formula *f*, if  $\mathcal{M}, w \vDash f$ , then  $\mathcal{M}, s \vDash f$ .

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#### **Proof Considerations**

- Proving  $\langle \forall \sigma: fp.\sigma.s : \langle \exists \delta: fp.\delta.w : match(B,\sigma,\delta) \rangle \rangle$  requires global reasoning; to do it mechanically is arduous.
- We define WFS and show it is equivalent to STS.
  - Soundness (easy).
  - Completeness (harder).
- Reasoning about WFS is *local.*
- The branching time result does not require machine closure, finite invisible nondeterminism, internal continuity, history variables, prophecy variables, etc.
- The linear time result only needs oracle variables.

#### **Refinement via Local Reasoning**

- Refinement requires matching infinite traces of the implementation and specification.
- Refinement maps allow a *local* check.
  - Technical overview:
- Define well-founded simulation & bisimulation.
- Local notions.
- Show soundness & completeness.



### Well-founded Simulation (WFS)

- 1.  $\langle \forall s, w \in S : sBw : L.s = L.w \rangle$
- 2. There are functions *rankt*:  $S^2 \rightarrow W$ , *rankl*:  $S^3 \rightarrow \mathbb{N}$  such that  $\langle W, \langle \rangle$  is well-founded and



$$\langle \forall s, u, w \in S : (sBw \land s \rightarrow u) :$$
  
 $\langle \exists v : w \rightarrow v : uBv \rangle$ 

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rankt(u,w) < rankt(s,w) rankl(v,s,u) < rankl(w,s,u)

 $\langle \forall s, u, w \in S : (sBw \land s \dashrightarrow u) : \\ \langle \exists v : w \dashrightarrow v : uBv \rangle$ 

v (uBw ^ rankt(u,w) < rankt(s,w)</pre>

 $\lor \langle \exists v : w \dashrightarrow v : sBv \land rankl(v,s,u) < rankl(w,s,u) \rangle \rangle$ 





rankl(v,s,u) = tag of v

Proof Outline.

1. Define rankt(s,w):

*tree*(*s*,*w*) is defined to be the largest subtree of the computation tree rooted at *s* s.t. at every non-root node  $\langle s, ..., x \rangle$ , we have *xBw* and  $\langle \forall v : w \rightarrow v : \neg(xBv) \rangle$ 





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Lemma: Every path of *tree* is finite.





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 $\lor \langle \exists v : w \dashrightarrow v : sBv \land rankl(v,s,u) < rankl(w,s,u) \rangle \rangle$ 

Lemma: Every path of *tree* is finite. Label nodes in *tree* using standard set-theory "rank" function. Lemma: If  $|S| \le \kappa$  (where  $\omega \le \kappa$ ), then for all s,w $\in$ S, tree(s,w) is labeled with an ordinal of cardinality  $\le \kappa$ .



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#### **Existence of Refinement Maps**

#### Branching time:

Theorem: If  $\mathcal{I}$  implements S, then there is a refinement map r such that  $\mathcal{I} \sqsubseteq_r S$ .

#### Linear time:

Theorem: If  $\mathcal{I}$  implements S (the set of traces of  $\mathcal{I}$  is a subset of the traces of S), then there exists  $\mathcal{I}$ , where  $\mathcal{I}$  is obtained from  $\mathcal{I}$  by adding an oracle variable, and a refinement map r such that  $\mathcal{I}' \sqsubseteq_r S$ .