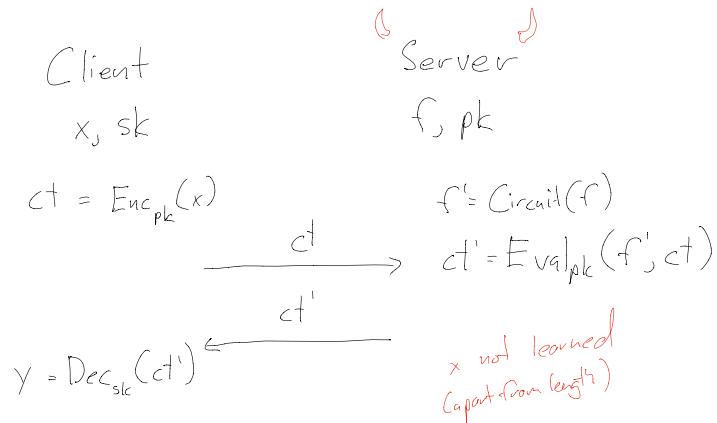


# FHE



To avoid the trivial soln, we insist that

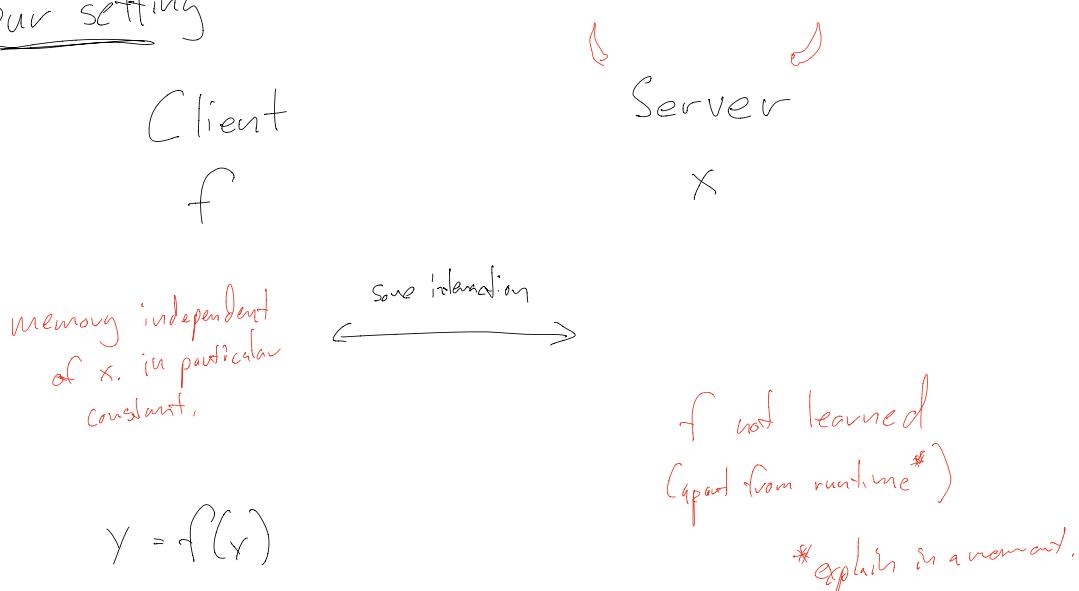
$$\exists \text{ polynomial } p \text{ s.t. } |\text{Circuit}(\text{Dec}_{sk}(\cdot))| = p(|sk|)$$

$\Rightarrow |ct'|$  independent of  $f$

$\Rightarrow$  Client work independent of  $f$

Notice: Client work *\*does\** depend on  $|x|$

## Our setting



## Tempting Solution

Client ( $f$ )

$$sk \leftarrow \$$$

$$f' = \text{Obf}(\text{Enc}_{sk}(f(x)))$$

Does not exist!

Very Slow

$$y = \text{Dec}_{sk}(ct)$$

$$f'$$

$$\text{Server}(x)$$

$$ct = f'(x)$$

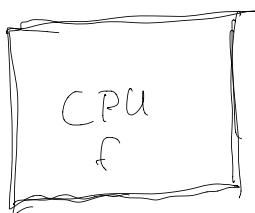
Also very slow!

$$ct$$

leaks output size

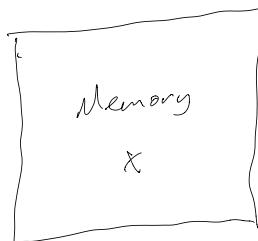
## Instead

Client ( $f$ )



$\xrightarrow{\quad}$   
 $\xrightarrow{\quad}$   
 $\xrightarrow{\quad}$   
 pointing to machine

Server( $x$ )



$$y = f(x)$$

\* leaving "routine"  
 = number of accesses  
 = max required memory

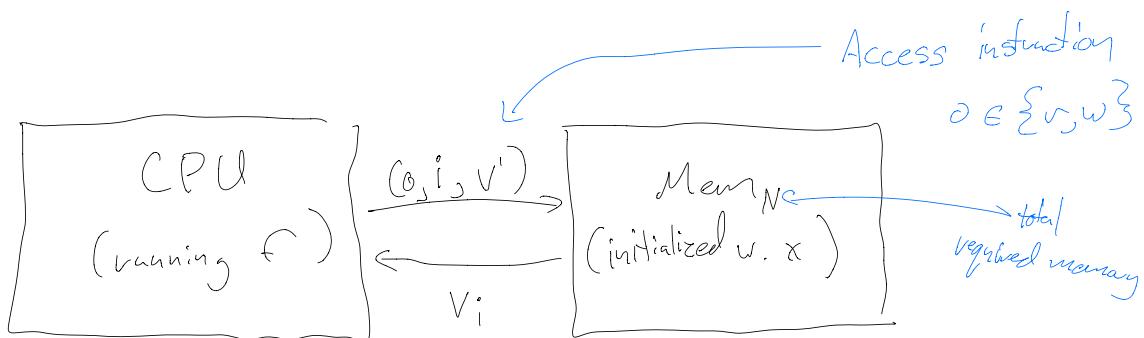
Model:

CPU has only a few registers (maybe const.)

f looks like a program on your computer

operations on register values

read/write to memory in order to store  
intermediate values.



What can a sequence of reads/writes reveal?

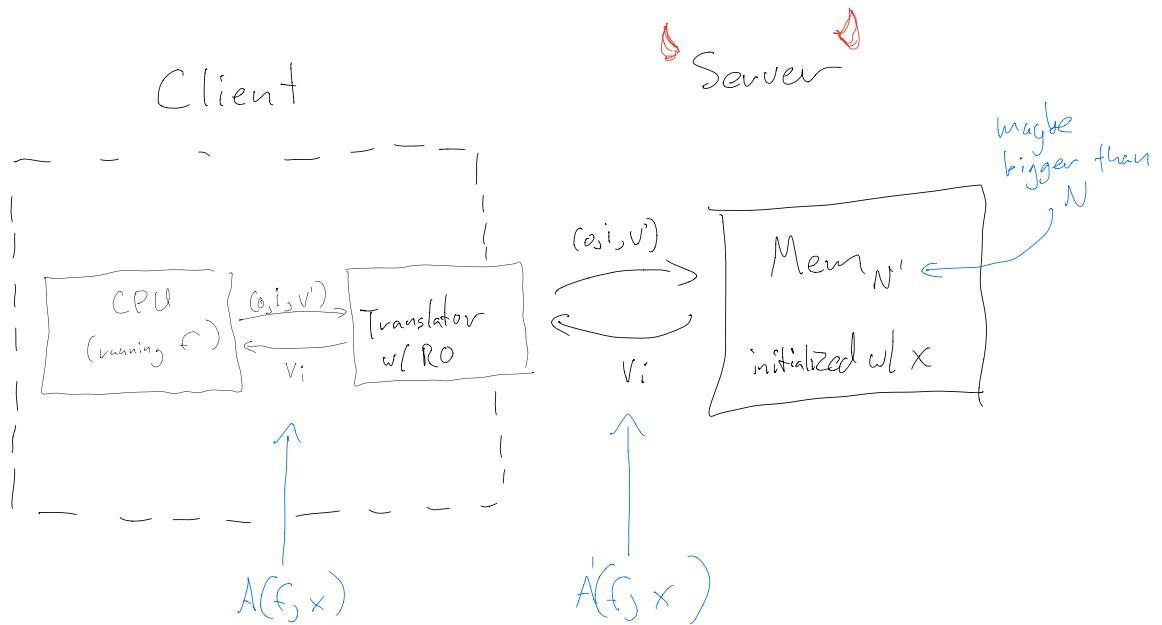
Everything! Many algs have characteristic access

patterns. Secrets could be baked into f;

x could be an encryption and accesses leak the  
plaintext.

ORAM ≡ Oblivious Random Access Machine /Memory

let  $A(f_j x) = a_0, a_1, \dots$   
 s.t.  $a_j$  is the  $j^{\text{th}}$  access instruction



## Security

$$\forall f^1, f^2, x^1, x^2$$

$$|A(f_j x^1)| = |A(f_j x^2)| \Rightarrow A'(f_j x^1) \approx A'(f_j x^2)$$

Alternately,  $\exists \text{ Sim}$  s.t.  $\forall f_j x$

$$A'(f_j x) \approx \text{Sim}(|A(f_j x)|)$$

Discuss Nomenclature

Discuss universal  $f_j$  hiding  $x$

Discuss Memory contents, hiding  $\circ$  and  $v^t$  and  $v_i$  *intuitively easy*

An oram client has an overhead  $g$  if  $\forall f_j x, T$  s.t.

$$|A(G_x)| = T, \quad |A'(f_j x)| = g(T) \cdot T + c \quad \begin{matrix} \leftarrow \text{initialization!} \\ \text{might depend on } k \end{matrix}$$

To Avoid trivial solutions we want  $g(T) < T$   
 $\downarrow$  strict less than

Also, the translator must use  $< N$  cells of local storage.

Simple and intuitive ORAM Formula:

At the beginning of each epoch, obliviously permute  
the memory randomly

To access element  $i$ :

access every element you have previously accessed this epoch. stash

if you have already found element  $i$  dummies help!

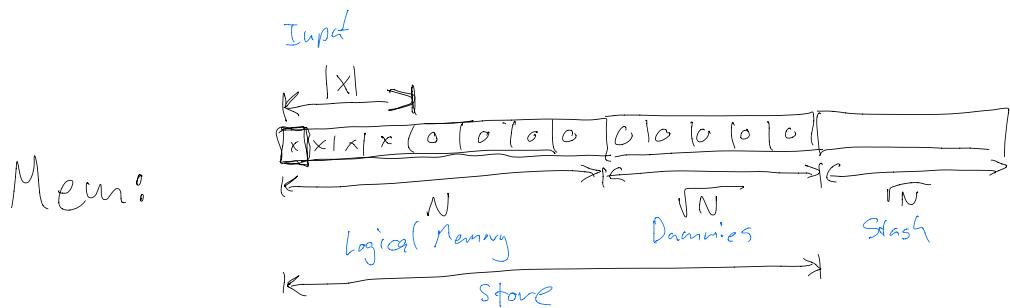
access an untouched element at random

else

compute the permuted location of  $i$  and access it.

At the end of each epoch, depermute the memory

# Square Root ORAM



CPU:

ctr	dctr	sk	$k$
reset each epoch	reset each epoch	random once	random once per epoch

+ space for two memory elements  
 $(\text{Enc}, \text{Dec})$  - randomized, symmetric

F - PRF

Initialization:

Add metadata  $O(N)$

Replace each  $j^{th}$  memory cell  $v$  with

$(\tilde{j} = \underbrace{\text{Enc}_{sk}(j)}_{\text{logical addr}}, \tilde{v} = \underbrace{\text{Enc}_{sk}(v)}_{\text{Data}}, b = \underbrace{\text{false}}_{\text{used bit}}) \leftarrow$  virtual memory cell  $i$

Epoch Start

Choose pub key  $k$  uniformly

Sort Mem + Dummies using bitwise compares on

$F_k(i_1)$  and  $F_k(i_2)$

AKS:  $O(N \log N)$

Batcher:  $O(N \log^2 N)$

let  $\text{ctr} = \text{dctr} = 0$

Access  $(i, o, v')$ :

For each  $(\tilde{i}, \tilde{v})$  in slash:  $O(\sqrt{N})$

if  $\text{Dec}(\tilde{i}) = i$ :

keep  $v_{out} = \text{Dec}(\tilde{v})$  as output

if  $o = \text{write encrypted } (i, v')$  and write back

else re-encrypt  $(i, v_{out})$  and write back

else

re-encrypt and write back

$\} O(\sqrt{N})$

If  $v_{out}$  was found:

let  $i' = F_k(N + \text{dcdr})$

$\text{dcdr} += 1$

else

let  $i' = F_k(i)$

Binary search store. For each node  $(\tilde{r}, \tilde{v}, b)$   $O(\log_2(N))$

if  $i' = F_k(\text{Dec}(\tilde{r}))$

if  $v_{out}$  not found, let  $v_{out} = \text{Dec}(\tilde{v})$

update  $b = \text{true}$

1

re-encrypt and store  $(\tilde{r}, \tilde{v})$  in slash 1

Epoch End

Move each slash element into a slave space where  $b = \text{true}$   $O(N)$

Sort Mem+Dummies using pairwise compares on

$\text{Dec}(\tilde{r}_1)$  and  $\text{Dec}(\tilde{r}_2)$

$O(N \log N)$

Correctness

Amortized Complexity

$$\begin{aligned} \text{Cost of epoch} & O(N \log N) \\ \div \text{Logical accesses} & O(\sqrt{N}) \\ = \text{Overhead} & O(\sqrt{N} \log N) \end{aligned}$$

Security

Permutation is indist from uniform = physical address are uniform

Perm is applied oblivious

Binary Search has deterministic access given target addr.

Each physical addr is searched  $\leq 1$  time

on each access, 1 stored item is searched

every stash item is visited once in order

1 new item is added to stash

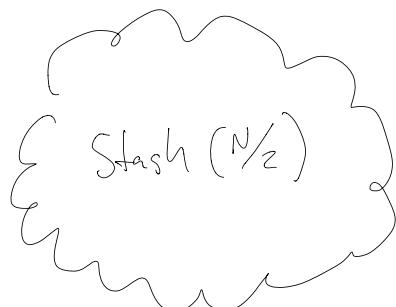
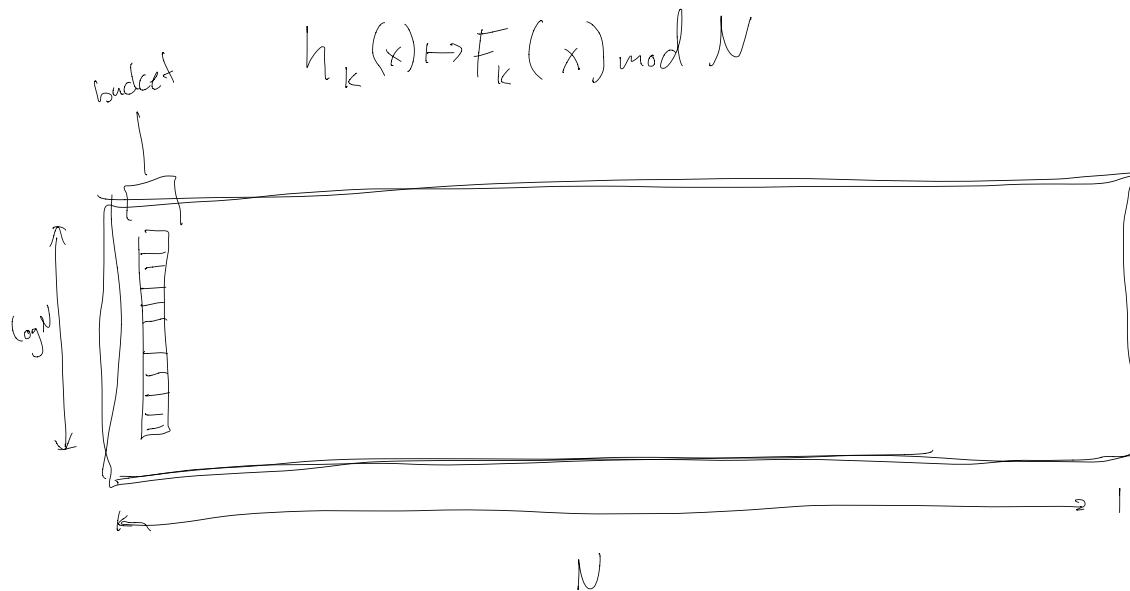
Where Does the unsatisfying overhead come from?

Observe it takes  $\sqrt{N}$  time to scan the stash, but we only need  $\leq 1$  element. What if the stash were in a smaller ORAM? Then we could make it larger and update less frequently

Problem: this one does not handle sparseness well.

Soln: modify construction to handle sparse indices

## Hierarchical ORAM



API: request index  $i$   
 (get back  $v$  or  $\perp$  if index  $i$  is erased)  
 save  $(i, v)$

GO slow how

Init / Beginning of epoch

Hash all elements into buckets by logical index  $O(N \log^2 N)$   
 Put indexless Dummies in all free space.

Access  $(i, o, v)$

Look for element  $i$  in stash and remove if it exists.  $O(\text{stash}(\frac{N}{\epsilon}))$   
 let  $i' = i$  if element  $i$  not found, else  $i' = \text{next ctr}$

Compute  $h_{k, \text{last}+1}(i')$ , scan bucket. Re-encrypt every value, and

if element  $i$  is found, replace it with a dummy.  $O(\log N)$

Save old or new value to stash as appropriate.

End of Epoch

Collect stashed values and remaining  $N/2$  stored values,  
remove dummies, begin again

Amortized Complexity

$$\begin{aligned} \text{Cost of epoch} &= O(N \log^2 N + N \cdot (\log N + \text{stash}(\frac{N}{2}))) \\ \div \text{Logical accesses} &= O(N) \\ = \text{Overhead} &= O(\log^2 N + \text{stash}(\frac{N}{2})) = O(\log^3 N) \end{aligned}$$

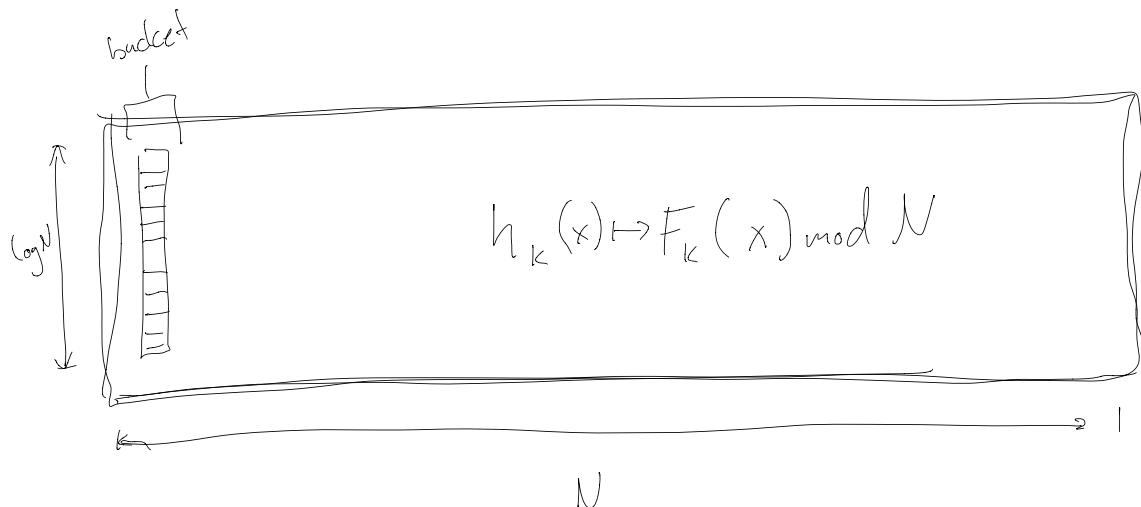
what goes  
here?  
why the same!

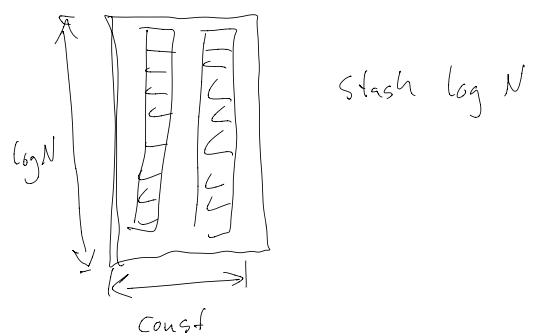
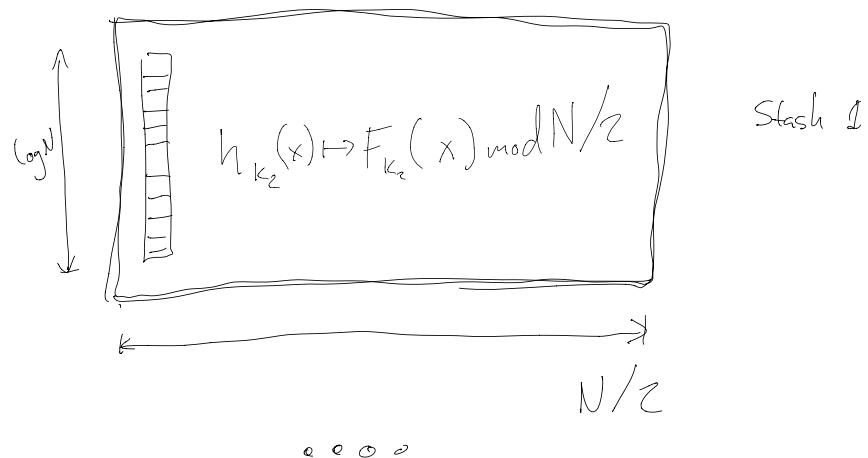
Base case: const. buckets. Always scan.

Security Note: Now we can use arbitrary sparse indices.

Querying an index not stored looks like querying one that is.

We must still touch no index more than once per epoch.

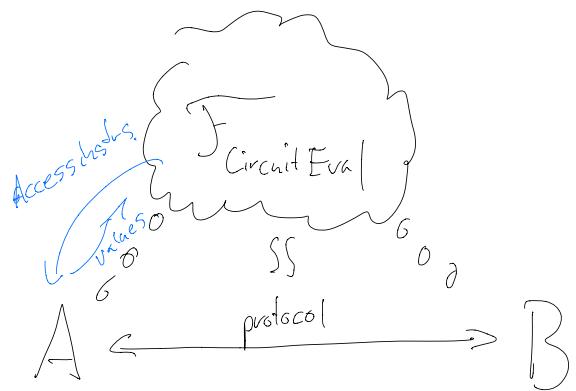




Overflow Probability:

$$\Pr[\log N \text{ collision}] \leq \binom{\log N}{\log \log N} \cdot \left(\frac{1}{N}\right)^{\log N - 1}$$

Application: MPC



Asymptotic efficiency gains. MPC sublinear in input size!

"Memory Gates"

Hide computed function's description?

Application: FHE

Open for a while... until